

IN THE CLAIMS

Please amend the claims as follows.

Claims 1-22 (previously cancelled).

Claims 23-32 (previously withdrawn).

Cancel claim 33 without prejudice to its subject matter.

34. (Currently amended) The amplifier of claim 3338, further comprising a feedback path from the output of the amplifier to the input of the gain stage to reduce the gain of the amplifier.

35. (Previously amended) The amplifier of claim 34, wherein the feedback path includes:

a first resistor coupled between the output of the amplifier and the input of the gain stage; and

a second resistor coupled from the input of the gain stage to a reference node.

36. (Previously presented) The amplifier of claim 35 for functioning as a voltage regulator, wherein the input signal includes a reference voltage applied to the second input of the gain stage to maintain the output signal at a constant potential.

37. (Previously presented) The amplifier of claim 36, wherein a difference between a supply voltage at the supply terminal and the constant potential of the output signal is less than 0.2 volts.

38. (Currently Amended) TheAn amplifier of claim 33, comprising:

a gain stage having an input for receiving an input signal and first and second outputs for providing a differential amplified signal; and

an output stage including first and second depletion mode transistors operating in response to the differential amplified signal and serially coupled between a supply terminal and an output of the amplifier for providing an output signal, wherein the first depletion mode transistor is an n-channel device having a drain coupled to the supply terminal and a source coupled to a node, and the second depletion mode transistor is a p-channel device having a source coupled to the node and a drain coupled to the output of the amplifier.